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Docket No. 0756-2344

6/1/2002 2823

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Art Unit: 2823
Shunpei YAMAZAKI et al.) Examiner: S. Foong
Serial No. 09/916,484)
Filed: July 30, 2001)
For: ELECTRIC DEVICE, MATRIX)
DEVICE, ELECTRO-OPTICAL DISPLAY)
DEVICE, AND SEMICONDUCTOR)
MEMORY HAVING THIN-FILM)
TRANSISTORS)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 7/26/02

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231
Sir:

Further to the Information Disclosure Statement filed on July 30, 2001, it was noted that the Form PTO-1449 submitted on July 30, 2001 listed JP 60-245172 with an abstract. However, Applicants submitted a full English translation with the Response filed on July 15, 2002. This reference is listed on the attached Form PTO-1449 with a full translation and Applicants respectfully request the Examiner to initial the attached Form PTO-1449 evidencing consideration thereof.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789